IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Chihiro UCHIBORI

In re application of:

Art Unit: 2825

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Serial No.:

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Examiner: G. LeeTECH CENTER 2800

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For:

METAL INTERCONNECTION, SEMICONDUCTOR DEVICE, METHOD FOR FORMING METAL INTERCONNECTION AND METHOD FOR

FABRICATING SEMICONDUCTOR DEVICE

RESPONSE UNDER 37 C.F.R. § 1.111

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 June 2, 2004

Sir:

This paper is filed in response to the Office Action dated December 2, 2003.

In the prosecution of the above-captioned patent application, please consider the following: